

10/574051

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

10/574051
AIA/5 REC'D/PCT/PTO 29 MAR 2006

INFORMATION DISCLOSURE STATEMENT		Docket Number 10191/4424	
Application Number To Be Assigned	Filing Date Herewith	Examiner To Be Assigned	Art Unit To Be Assigned
Invention Title SYSTEM AND METHOD FOR TESTING CONTROL PROCESSES IN A VEHICLE		Inventor(s) PILLIN et al.	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. The references are listed on the attached modified PTO form 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 3/26/06

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INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO FORM 1449	Atty. Docket No. 10191/4424	Serial No. 10/574051 To Be Assigned
	Applicant(s) PILLIN et al.	
	Filing Date Herewith	Group To Be Assigned

U. S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
/Y.A./	2003/0131964*	Jul. 17, 2003	SANTORI et al.			

* Copy of reference is not enclosed because reference is cited in Search Report (copy of reference provided by International Searching Authority).

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	ALOKHIN VADIM: "Model-Based Implementation of Embedded SW Systems ASCET-SD", Munich Technical University, Faculty of Computer Science, Munich, Found on the Internet, 1/14/2005, Summer semester 2003, pgs. 1-18.*
	GEYER HARTMUT: "Diploma Thesis: Integrated Model-Based Design of Embedded Systems With Reference to the Example of a Traffic Control System", Vienna Technical University, Faculty of Computer Science and Automation, Institute for Theoretical and Technical Computer Science, Found on the Internet, 1/14/2005, entire document.*
	REBESCHIESS S: "MICROS – Microcontroller-Based Real Time Control System Toolbox For Use With Matlab/Simulink", Computer-Aided Control System Design, 1999, Proceedings of the 1999 IEEE International Symposium of Kohala Coast, HI, Aug 22-27, 1999, pgs. 267-72.*
	QERTEL K: "Code By Click – From the Idea to the Finished Product Using ASCET-SD 3.0", ELECTRONICS INDUSTRY, April 1999, pages 95-6, entire document.*

* Copy of reference is not enclosed because reference is cited in Search Report (copy of reference provided by International Searching Authority).

EXAMINER /Yogesh Aggarwal/	DATE CONSIDERED 05/25/2009
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	